

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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I hereby certify that this correspondence has been e-filed with the U.S. Patent and Trademark Office on October 10, 2011.

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Reg. No. 48,017

In re application of:	:	
Chamberlain et al.	:	
	:	
Serial No.: 10/550,323	:	Examiner: Fleurantin, Jean B.
	:	
Filed: September 1, 2006	:	Group Art Unit: 2162
	:	
For: Intelligent Data Storage and Processing	:	
Using FPGA Devices	:	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

LETTER TO PTO

Applicant submits herewith a replacement set of drawings in response to the Notice Regarding Drawings mailed September 12, 2011. The figure number label has been added to Figure 18, and a formal version of hand-drawn Figure 43 is also provided. Furthermore, Applicant provides herewith formal versions of Figures 29-42, 44-46, and 48-50 to replace the previous hand-drawn versions of those figures. No new matter has been added.

Respectfully submitted,

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